AMENDMENTS TO THE CLAIMS

1. (Currently Amended) A liquid crystal display device comprising:

a first substrate;

a second substrate;

a liquid crystal layer between the first and second substrates;

a plurality of gate lines and data lines on the first substrate to define pixel regions;

a common auxiliary electrode on the first substrate to surround the pixel regions;

A1

a pixel electrode having at least one or more electric field induction windows on the first [substrates] substrate; [and]

a common electrode on the second substrate; and

at least one or more dielectric structures on the common electrode;

wherein the dielectric structures extend from the second substrate to the first substrate;

and wherein the dielectric structures act as a spacer between the first and second substrates.

2. (Original) The device of claim 1, wherein the common auxiliary electrode is on a layer equal to the gate lines.

3. (Canceled)

4. (Canceled)

A2

5. (Original) The device of claim 1, wherein each of the pixel regions is divided into one or more sections to form a multi-domain pixel.

6. (Original) The device of claim 5, wherein the one or more sections of the pixel regions have different driving characteristics.

7. (Original) The device of claim 1, further comprising a common auxiliary electrode formed in the electric field induction windows.

8. (Currently Amended) The device of claim 1, wherein the electric field induction windows include slits.

9. (Original) The device of claim 1, wherein the common auxiliary electrode partially overlaps the pixel electrode.

10. (Original) The device of claim 1, further comprising an alignment film on at least on of the first and second substrates.

11. (Original) The device of claim 1, further comprising a phase difference film on at least one of the first and second substrates.

12. (Currently Amended) A liquid crystal display device comprising:

a first substrate;

a second substrate;

a liquid crystal layer between the first and second substrates;

a plurality of gate lines and data lines on the first substrate to define pixel regions;

a common auxiliary electrode on the first substrate to surround the pixel regions;

a pixel electrode having at least one or more electric field induction windows on the first substrate; and

at least one or more dielectric structures on the pixel electrode;

wherein the dielectric structures extend from the first substrate to the second substrate and

wherein the dielectric structures act as a spacer between the first and second substrates.

13. (Original) The device of claim 12, wherein the common auxiliary electrode is on a layer equal to the gate lines.

14. (Canceled)

15. (Canceled)

16. (Original) The device of claim 12, wherein each of the pixels is divided into one or more sections to form a multi-domain pixel.

17. (Original) The device of claim 16, wherein the one or more sections of the pixel regions have different driving characteristics.

18. (Original) The device of claim 12, further comprising a common auxiliary electrode formed in the electric field induction windows.

19. (Original) The device of claim 12, wherein the electric field induction windows include slits.

20. (Original) The device of claim 12, wherein the common auxiliary electrode partially overlaps the pixel electrode.

21. (Original) The device of claim 12, further comprising an alignment film on at least one of the first and second substrates.

A3 end

22. (Original) The device of claim 12, further comprising a phase difference film on at least one of the first and second substrates.